

**In the Specification:**

Please replace paragraph 0027 with the following amended paragraph:

Referring to **Figure 3**, network controller 221 includes a baseband 300, a media access control layer (MAC) 310, a digital to analog converter (DAC) 345, an analog to digital converter (ADC) 348, a radio frequency (RF) transceiver 350, and an antenna 360. In one embodiment, DSP 300 is an embedded DSP. According to one embodiment, baseband 300, MAC 310, and DAC 345 and ADC 348 are controlled by separate embedded digital signal processors 301, 302, 303 and 304, respectively (DSPs). An embedded DSP typically integrates a processor core, a program memory device, and application-specific circuitry on a single integrated circuit die. One of ordinary skill in the art will appreciate that one or more of the DSPs may be replaced with other components (e.g., field programmable gate arrays (FPGAs) without departing from the scope of the invention).